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Theodore Allen, Liyang Yu

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Jack P. C. Kleijnen
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8 Session 1D: Analog macromodeling: Simulation-based automatic generation of signomial and posynomial performance models for analog integrated circuit sizing
Walter Daems, Georges Gielen, Willy Sansen
November 2001 **Proceedings of the 2001 IEEE/ACM international conference on Computer-aided design**
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This paper presents a method to automatically generate posynomial response surface models for the performance parameters of analog integrated circuits. The posynomial models enable the use of efficient geometric programming techniques for circuit sizing and optimization. To avoid manual derivation of approximate symbolic equations and subsequent casting to posynomial format, techniques from design of experiments and response surface modeling in combination with SPICE simulations are used to gene ...

Keywords: analog circuit modeling, design of experiments, geometric programming, posynomial and signomial response surface modeling

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Susan M. Sanchez, Thomas W. Lucas
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12 Server performance and scalability: A smart hill-climbing algorithm for application server configuration
Bowei Xi, Zhen Liu, Mukund Raghavachari, Cathy H. Xia, Li Zhang
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Keywords: automatic tuning, gradient method, importance sampling, simulated annealing, system configuration

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E. G. A. Gaury, J. P. C. Kleijnen
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Hongzhou Liu, Amit Singhee, Rob A. Rutenbar, L. Richard Carley
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The introduction of simulation-based analog synthesis tools creates a new challenge for analog modeling. These tools routinely visit 103 to 105 fully simulated circuit solution candidates. What might we do with all this circuit data? We show how to adapt recent ideas from large-scale data mining to build models that capture significant regions of this visited performance space, parameterized by variables manipulated by synthesis, trained by the

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18 Hierarchical statistical characterization of mixed-signal circuits using behavioral modeling
Eric Felt, Stefano Zanella, Carlo Guardiani, Alberto Sangiovanni-Vincentelli
January 1997 **Proceedings of the 1996 IEEE/ACM international conference on Computer-aided design**
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A methodology for hierarchical statistical circuit characterization which does not rely upon circuit-level Monte Carlo simulation is presented. The methodology uses principal component analysis, response surface methodology, and statistics to directly calculate the statistical distributions of higher-level parameters from the distributions of lower-level parameters. We have used the methodology to characterize a folded cascode operational amplifier and a phase-locked loop. This methodology permi ...

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William F. Atchison, Samuel D. Conte, John W. Hamblen, Thomas E. Hull, Thomas A. Keenan, William B. Kehl, Edward J. McCluskey, Silvio O. Navarro, Werner C. Rheinboldt, Earl J. Schweppe, William Viavant, David M. Young
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2. **Risk analysis of robust system design**
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DOCUMENT-IDENTIFIER: US 20050084076 A1

TITLE: Programmable logic device for wireless local area network

PUBLICATION-DATE: April 21, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dhir, Amit	San Jose	CA	US	
Rangasayee, Krishna	Pleasanton	CA	US	

US-CL-CURRENT: 379/55.1

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TITLE: Method and system for optimizing the design of a mechanical system

PUBLICATION-DATE: January 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dhir, Anil Kumar	Ann Arbor	MI	US	
Crane, Scott Patrick	Louisville	KY	US	

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DOCUMENT-IDENTIFIER: US 5291943 A

TITLE: Heat transfer enhancement using tangential injection

DATE-ISSUED: March 8, 1994

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dhir; Vijay K.	Santa Monica	CA		

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US-PAT-NO: 3553463
DOCUMENT-IDENTIFIER: US 3553463 A

TITLE: RADIATION SENSITIVE DOCUMENT SCANNING APPARATUS USING HELICAL SCANNER

DATE-ISSUED: January 5, 1971

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dhir; Partap C.	Penfield	NY		
Woodhull; Elliot H.	Rochester	NY		

US-CL-CURRENT: 250/570; 356/71, 358/491

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw Desc | Image

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Term	Documents
DHIR	1041
DHIRS	2
MECHANICAL	1006903
MECHANICALS	248

Hit List

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Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 20050004833 A1

Using default format because multiple data bases are involved.

L3: Entry 1 of 8 File: PGPB Jan 6, 2005

PGPUB-DOCUMENT-NUMBER: 20050004833
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050004833 A1

TITLE: Method and system for integrated uncertainty analysis

PUBLICATION-DATE: January 6, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
McRae, Gregory J.	Winchester	MA	US	
Wang, Cheng	San Diego	CA	US	

US-CL-CURRENT: 705/11

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

☐ 2. Document ID: US 20030113813 A1

L3: Entry 2 of 8 File: PGPB Jun 19, 2003

PGPUB-DOCUMENT-NUMBER: 20030113813
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030113813 A1

TITLE: Methods and devices for the integrated discovery of cell culture environments

PUBLICATION-DATE: June 19, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Heidaran, Mohammad A.	Cary	NC	US	
Meyer, Mary K.	Durham	NC	US	
Rowley, Jon A.	Chapel Hill	NC	US	
Hemperly, John J.	Apex	NC	US	

US-CL-CURRENT: 435/7.2; 435/287.2, 435/325

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

☐ 3. Document ID: US 20030009317 A1

L3: Entry 3 of 8 File: PGPB Jan 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030009317
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030009317 A1

TITLE: Method and system for optimizing the design of a mechanical system

PUBLICATION-DATE: January 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dhir, Anil Kumar	Ann Arbor	MI	US	
Crane, Scott Patrick	Louisville	KY	US	

US-CL-CURRENT: 703/7

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

☐ 4. Document ID: US 20020147577 A1

L3: Entry 4 of 8 File: PGPB Oct 10, 2002

PGPUB-DOCUMENT-NUMBER: 20020147577
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020147577 A1

TITLE: System and method for performing compound computational experiments

PUBLICATION-DATE: October 10, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bankes, Steven C.	Los Angeles	CA	US	

US-CL-CURRENT: 703/22

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

☐ 5. Document ID: US 6389380 B1

L3: Entry 5 of 8 File: USPT May 14, 2002

US-PAT-NO: 6389380
DOCUMENT-IDENTIFIER: US 6389380 B1

TITLE: System and method for performing compound computational experiments

DATE-ISSUED: May 14, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bankes; Steven C.	Los Angeles	CA		

US-CL-CURRENT: 703/17; 703/1, 703/2, 706/45

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw Desc | Image

☐ 6. Document ID: US 6366822 B1

L3: Entry 6 of 8 File: USPT Apr 2, 2002

US-PAT-NO: 6366822
DOCUMENT-IDENTIFIER: US 6366822 B1

TITLE: Statistical process window design methodology

DATE-ISSUED: April 2, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Heavlin; William D.	El Granada	CA		

US-CL-CURRENT: 700/31; 700/121

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw Desc | Image

☐ 7. Document ID: US 5655110 A

L3: Entry 7 of 8 File: USPT Aug 5, 1997

US-PAT-NO: 5655110
DOCUMENT-IDENTIFIER: US 5655110 A
**** See image for Certificate of Correction ****

TITLE: Method for setting and adjusting process parameters to maintain acceptable critical dimensions across each die of mass-produced semiconductor wafers

DATE-ISSUED: August 5, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Krivokapic; Zoran	Santa Clara	CA		
Heavlin; William D.	San Francisco	CA		
Kyser; David F.	San Jose	CA		

US-CL-CURRENT: 716/19; 700/95, 703/13

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw Desc | Image

☐ 8. Document ID: US 5646870 A

L3: Entry 8 of 8 File: USPT Jul 8, 1997

US-PAT-NO: 5646870
DOCUMENT-IDENTIFIER: US 5646870 A
**** See image for Certificate of Correction ****

TITLE: Method for setting and adjusting process parameters to maintain acceptable critical dimensions across each die of mass-produced semiconductor wafers

DATE-ISSUED: July 8, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Krivokapic; Zoran	Santa Clara	CA		
Heavlin; William D.	San Francisco	CA		
Kyser; David F.	San Jose	CA		

US-CL-CURRENT: 716/4; 700/117, 700/31, 703/13, 716/20

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw Desc | Image

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Term	Documents
LATIN	3808
LATINS	1
HYPERCUBE	1439
HYPERCUBES	354
SAMPLING	200615
SAMPLINGS	4604
((LATIN ADJ HYPERCUBE ADJ SAMPLING) AND 2).PGPB,USPT.	8
(L2 AND LATIN HYPERCUBE SAMPLING).PGPB,USPT.	8

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Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 20030009317 A1

Using default format because multiple data bases are involved.

L4: Entry 1 of 3 File: PGPB - Jan 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030009317
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030009317 A1

TITLE: Method and system for optimizing the design of a mechanical system

PUBLICATION-DATE: January 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dhir, Anil Kumar	Ann Arbor	MI	US	
Crane, Scott Patrick	Louisville	KY	US	

US-CL-CURRENT: 703/7

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

☐ 2. Document ID: US 5655110 A

L4: Entry 2 of 3 File: USPT Aug 5, 1997

US-PAT-NO: 5655110
DOCUMENT-IDENTIFIER: US 5655110 A
**** See image for Certificate of Correction ****

TITLE: Method for setting and adjusting process parameters to maintain acceptable critical dimensions across each die of mass-produced semiconductor wafers

DATE-ISSUED: August 5, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Krivokapic; Zoran	Santa Clara	CA		
Heavlin; William D.	San Francisco	CA		
Kyser; David F.	San Jose	CA		

US-CL-CURRENT: 716/19; 700/95, 703/13

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw Desc | Image

☐ 3. Document ID: US 5646870 A

L4: Entry 3 of 3 File: USPT Jul 8, 1997

US-PAT-NO: 5646870
DOCUMENT-IDENTIFIER: US 5646870 A
** See image for Certificate of Correction **

TITLE: Method for setting and adjusting process parameters to maintain acceptable critical dimensions across each die of mass-produced semiconductor wafers

DATE-ISSUED: July 8, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Krivokapic; Zoran	Santa Clara	CA		
Heavlin; William D.	San Francisco	CA		
Kyser; David F.	San Jose	CA		

US-CL-CURRENT: 716/4; 700/117, 700/31, 703/13, 716/20

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw Desc | Image

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Term	Documents
LATIN	3808
LATINS	1
HYPERCUBE	1439
HYPERCUBES	354
SAMPLING	200615
SAMPLINGS	4604
RESPONSE	1195465
RESPONSES	152740
SURFACE	2674833
SURFACES	1297218
MODEL$	0
(L2 AND LATIN HYPERCUBE SAMPLING AND RESPONSE SURFACE MODEL$).PGPB,USPT.	3

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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 20030009317 A1

Using default format because multiple data bases are involved.

L5: Entry 1 of 1 File: PGPB Jan 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030009317
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030009317 A1

TITLE: Method and system for optimizing the design of a mechanical system

PUBLICATION-DATE: January 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Dhir, Anil Kumar	Ann Arbor	MI	US	
Crane, Scott Patrick	Louisville	KY	US	

US-CL-CURRENT: 703/7

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw Desc | Image

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Term	Documents
MECHANICAL	1006903
MECHANICALS	248
SYSTEM	2563444
SYSTEMS	1546205
LATIN	3808
LATINS	1
HYPERCUBE	1439
HYPERCUBES	354
SAMPLING	200615
SAMPLINGS	4604
RESPONSE	1195465
((MECHANICAL WITH SYSTEM) AND DESIGN$ AND OPTIMIZ$ AND LATIN HYPERCUBE SAMPLING AND RESPONSE SURFACE	1